

LAKSHMI BARLA

D.No:18-88, FF – II,
Salomi Residency,
Adithya Nagar,
Madhurawada,
Visakhapatnam – 48.

E-mail : blakshmi@gvpcew.ac.in
: lakshmibarla207@gmail.com
Mobile : +91-9493915870

Career Objective :

To work in a creative, challenging environment and achieve growth oriented career in an organization where I can contribute best to the growth of the organization and my technical skills.

Teaching Experience

- ◆ Currently working as Assistant Professor in Department of ECE in Gayatri Vidya Parishad College of Engineering for Women from August 2011 to till date.
- ◆ Worked as Assistant Professor in Department of Electronics & Communication Engineering, Viswanadha Institute of Technology and Management from July 2004 to October 2008.

Academic Record :

Qualification	School/College	University/Board	Period	Percentage of Marks
M.Tech (VLSI System Design)	St. Therissa college of Engineering, Garividi, Vizianagaram.	J.N.T.U,Kakinada	2008-2010	71
B.Tech (ECE)	SISTAM College of Engineering, Srikakulam	J.N.T.U,Kakinada	1999-2003	66
DIPLOMA (ECE)	Govt. Polytechnic Narsipatnam	State Board of Technical Education & Training	1995-1998	69
S.S.C	A.P. Residential School, Peddapuram, E.G	Board of Secondary Education,A.P.	1994-1995	75

Technical Skills :

- ◆ Languages : C, VHDL, Verilog
- ◆ Software Packages : MATLAB and Mentor Graphics

Personality Traits:

- ◆ Ability to work in teams.
- ◆ Adaptability to new environments.
- ◆ Ability to work in time bound crisis situations.
- ◆ Hard working and result oriented.

Member Ship:

- ◆ Member of Indian Society for Technical Education.

Subjects Taught :

Digital IC Applications	VLSI Design
Low power VLSI Design	Electronic Devices & Circuits
Pulse & Digital Circuits	Digital Logic Design
Linear Integrated Circuit Applications	Digital System Design
Switching Theory and Logic Design	Electronic Circuit Analysis
Analog Communications	Digital Communications
CMOS Mixed Signal Circuit Design	CMOS Analog IC Design
Design of Fault Tolerant Systems	Signals & Systems

Online Courses (NPTEL):

S.No.	Title of Lecture	Organization Details	Duration & Dates	Score
1	ANALOG CIRCUITS	NPTEL	12 WEEKS 31/01/2018 - 21/04/2018	81%
2	DIGITAL CIRCUITS	NPTEL	12 WEEKS (JULY- OCT,2018)	80%
3	MICROELECTRONICS:DEVICES TO CIRCUITS	NPTEL	12 WEEKS (JULY- OCT,2019)	86%

Conferences & Publications :

- ◆ B Lakshmi, M Kama Raju, K Babulu “Subthreshold Region based Linear Feedback Shift Register” International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 – 8958, Volume-8, Issue-6S2, August 2019.
- ◆ B Lakshmi, Dr. M Kama Raju, Dr. K Babulu “Design and implementation of Linear Feedback Shift Register in Subthreshold Region” International Conference on Computational and Intelligent Techniques for Automation of Engineering.
- ◆ K.Venkata laxmi, B.Lakshmi“ Power Efficient and Noise Immune Domino Logic for Wide Fan in Gates”(IAEEIE)International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 3, Issue 10, October 2014

FDP's & Workshops:

- A Two day online Workshop on “Communication, VLSI, Image processing & design of Antennas ” organized by UCEV , Vizianagaram, during 3-9-2020 to 4-9-2020.
- International FDP on “Deep learning algorithms for computer Vision Applications” organized by BITS, Vizag.

- National level One Week FDP on “Resent advances in VLSI & Embedded Systems” organized by KLU, Guntur, during 14th to 19th september,2020.
- One Week online National FDP on “Mathematical Elements In Engineering And Applied Sciences (Meeas-2020)” organized by Gayatri Vidya Parishad College of Engineering for Women, Visakhapatnam, during 21-07-2020 to 25-07-2020.
- A 5 Day Workshop on Fight Covid 19 using “IoT” organised by GVPCEW in association with Enovate Skill during 6th to 10th July, 2020.
- A five days online FDP on “ Current Research Trends in Electronics & Communication Engineering” organized by RGM College Of Engineering & Technology during 29th June to 3rd July 2020.
- A three day FDP on “ The way to Publish the papers in SCI Index journals” organized by RGM College Of Engineering & Technology during 29th June to 3rd July 2020.
- A Five-day National Level Online Faculty Development Program on “Trends in Electronics and Communication Engineering”, organized by Sri Vasavi Engineering College, Tadepalligudem, during 22nd to 26th June 2020.
- A Five-day National Level Online Faculty Development Program on “Scilab and NgSpice”, organized by Universal College of Engineering & Technology during 22nd to 26th June 2020.
- A Two-day Online Faculty Development Program on “Effective Technical Report Writing using LaTeX”, organized by M G I T, Hyderabad, during 8th to 9th June 2020.
- A Five-day National Level Online Faculty Development Program on “Skill Development for effective Research and Teaching – Learning Process” , organized by Anil Neerukonda Institute of Technology & Sciences (ANITS), Visakhapatnam, during 8th to 12th June 2020.
- A Six-day National Level Online Knowledge Development Program on “Challenges and advancements in the Design of IoT, Embedded and VLSI systems: A Researchers View ”, organized by Gudlavalleru Engineering College, Gudlavalleru, during 8th to 13th June 2020.
- One Week National level online FDP on “Research challenges and Emerging Trends In Electronics and Communication engineering” organized by Ramachandra College of Engineering, Eluru, during 27th May to 1st June ,2020.
- A five days online FDP on “ Trends in Communication” organized by Sir C R Reddy College Of Engineering , Eluru, during 26th to 30th May ,2020.
- A 5 Day Skill Development Program on “ Design, Implementation and Verification in VLSI ” organized by Sandeepani School of Embedded System Design, Bangalore during 27th Apr to 1st May 2020.
- A One and Half week FDP on “Microelectronics:Devices To Circuits”, Organized by the IIT Roorkee.
- A two week summer FDP on Important Engineering Subjects, Organized by the JNTUK Kakinada ,2019.
- A three day National workshop on “Cryptology and Cyber Security”, GVPCEW, 2018.
- A three day workshop on "Custom Analog and Digital IC Design using Mentor EDA Tools", GVPCEW, 2018.
- A Two- week ISTE STTP on“CMOS, Mixed Signal and Radio frequency VLSI Design” Conducted by Indian Institute of Technology Kharagpur, 2017.
- A Two day Workshop on “Signal Processing and Communication Systems” Organized by the GVPCEW,2017.
- Workshop on "EMRF" conducted at Gayatri Vidya Parishad College of Engineering for Women in 2016.

- Workshop on "Cognitive Radio Technologies" conducted at Gayatri Vidya Parishad College of Engineering for Women in 2015.
- Workshop on "VLSI & EDA Tools" conducted at JNTU-Vizianagram in 2013.
- Workshop on "System Design Using XILINX FPGA's" conducted at Gayatri Vidya Parishad College of Engineering in 2011.
- Workshop on "VLSI & Embedded Systems" conducted at Viswanadha Institute of Technology and Management in 2009.
- Workshop on "Signal processing Evolutionary computational and modelling" conducted at DIETCE, Anapalli in 2009.
- Workshop on "Recent Developments in information & Communications Technologies" conducted at AITAMCE, Tekkali in 2008.

Webinars:

- A one day webinar on "Industrial IoT & AI/ML Job Opportunities", organized by GVPCEW in association with RUGGEDBOARD and PHYTEC Embedded Pvt. Ltd. On 24th september,2020.
- A one day webinar on "Campus to Corporate", organized by IBS business school, Bengalore on 29th August 2020.
- A National level webinar on "A National Level Webinar on "Intellectual Property Rights & Patents - A View", organized by Gudlavalleru Engineering College, Gudlavalleru, on 10 - 12 August 2020.
- A one day webinar on "Scope in Automotive Industry", organized by RGM College Of Engineering & Technology, Kurnool on 21/07/2020.
- A one day webinar on "Women Emporment", organized by Mahatma Gandhi Institute of Technology, Kurnool on 01/07/2020 .
- A one day webinar on "Modern Trends in Mobile Communication", organized by Audisankara Group of Institutions, Gudur, on 26th June 2020.
- A one day webinar on "Image Noise Reduction System", organized by Audisankara Group of Institutions, Gudur, on 22nd June 2020.
- A Two day webinar on "Tips to formulate Research project proposals and writing technical papers", organized by G M R Institute of Technology, Rajam, during 21st to 22nd June 2020.
- A one day webinar on "Lossless Medical Image Compression", organized by Audisankara Group of Institutions, Gudur, on 20th June 2020.
- A one day webinar on "Communication in Sensor Networks: A Nitty-Gritty", organized by K L E F, Guntur, on 19th June 2020.
- A one day webinar on "VLSI Design and verification", organized by G V P C E W, Visakhapatnam, on 3rd June 2020.
- "Step into Robotic Process Automation", organized by GUVI tech, on 30th May,2020.
- A one day webinar on "Research publications, Process, Credentials and Outcomes", organized by BITS, Vizag on 9th May,2020.
- A Webinar on "Analog IC Design Using Mentor EDA Tools " held on 10th April 2020 in association with CoreEL Technologies and Mentor.

Personal Profile :

Date of birth : 05 – 12 – 1978
Father's name : B Appala Naidu
Gender : Female
Marital status : Married
Languages known : English and Telugu
Nationality : Indian.

Declaration :

I here by declare that all the information mentioned above is true to the best of my knowledge.

Date: 11-05-2022

Place: Visakhapatnam

[BARLA LAKSHMI]